EE Call Letter List

M Tech. (Research), 2025 - 26

The following applicants have been selected for written exam and/or interview for the department for the department of Electrical and Electronics Engineering for M Tech. (Research) Programme. The applicants are requested to go through additional information provided in their Call letters.

#	Name	Reference Number	Branch/Specialisation
1	Sreenath Krishnan K R	MTR2025PEC0001	Power Electronics and Control
2	Meravath Gayathri	MTR2025PEV0008	Power Electronics and Control for Electric Vehicle
3	Yash Soni	MTR2025PEC0002	Power Electronics and Control

Head Of Department
Electrical and Electronics Engineering

विभागाध्यक्ष/H.O.D.
वि. एवं इ. अभि. विभाग/Dept. of EEE
राष्ट्रीय प्रौद्योगिकी संस्थान कर्नाटक
National Institute of Technology Karnataka
सुरत्कल, मंगलूरु – ५७५ ०२५/Surathkal, Mangaluru - 575 025
कर्नाटक, भारत/Karnataka, India

Dean Academics

Tel : 0824-2473550
Fax : 0824-2474039
Website : www.eee.nitk.ac.in

Date: 21st November 2025

To the shortlisted candidates:

With reference to your application for admission to **M.Tech. (Research) Programme in the Department of Electrical and Electronics Engg.,** you are requested to appear **physically/offline** before the selection committee at your own cost for M.Tech. (Research) admission selection process. You should produce all records such as Date of Birth Certificate, Degree Certificate, Marks Cards of all years/semesters (U.G. Programme), Class 10th & 12th, GATE Score Card, Diploma Marks Card, Sponsorship certificate (if applicable), SC/ST Certificate (if applicable), OBC Certificate issued on or after 01 April 2025 (if applicable), EWS Certificate issued on or after 01 April 2025 (if applicable), Persons with Disabilities (PWD) certificate (if applicable), Conduct certificate and Testimonials, and valid photo identity card, **(All originals)**. Please bring a self-attested photocopy of each of these certificates to submit at the time of interview.

Department	: Electrical and Electronics
Place of Interview	: Dept. of Electrical and Electronics Engg., Western Side Campus, NITK, Surathkal
Registration	: 09-12-2025, 2:00 PM at Seminar Hall, Dept. of E & E
Document Verification	: 09-12-2025, 2:30 PM at Seminar Hall, Dept. of E & E
Written Test	: 09-12-2025, 03:30 PM to 4:30 PM at Seminar Hall, Dept. of E & E
Announcement of Shortlisted Candidates for Interview*	: 09-12-2025, 05:30 PM
*(provisional; pending document verification)	
Interview	: 10-12-2025, 09:30 AM onwards at Conference Room, Dept. of E & E

Note:

1. **Please see the institute eligibility criteria given in the advertisement, carefully. It is the sole responsibility of the candidate to ascertain his/her eligibility before coming for the interview. The final confirmation of your eligibility is done only after the verification of original certificates.**

2. Candidates are required to go through the list of Instructions to Candidates which is enclosed herewith.

3. Candidates must bring the "**Data Collection Format**" (enclosed) **duly filled** and submit at the time of registration/verification of documents on **9th December 2025**.

4. **Full-time/External Registrants** - sponsored from Industry or other organizations including Educational Institutions, **should have been serving in the sponsoring organization for a period of at least 2 years after qualifying degree** and have to produce a letter from their employer stating that the candidate is deputed for Research Programme (Full time/Part time) of the Institute on **full salary,** during the study period. The employer should indicate that the candidate will not be withdrawn midway before the completion of the course. (Sponsorship letter should be in the format provided in the Application Form).

5. Candidates who have not submitted marks of final examination along with application form shall produce the same at the time of admission.

6. Candidates who have got the degree(s) from the other Universities (Other than NITK) have to produce Migration certificate in order to validate their admission at a later stage.

7. The Selected candidates are required to pay fees and get admitted to the institute as per dates specified by the institute. Fee details are as given in the "Information bulletin for Ph.D & M.Tech Programme 2025-26 (December Session)".

(HoD & Chairman DPGC, EED)
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NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA-SURATHKAL
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Date: 21st November 2025

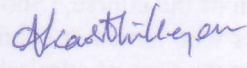## INSTURCTIONS TO CANDIDATES APPLIED FOR M.Tech. (Research)PROGRAMME.

Dear Candidates,

You are invited to participate physically/offline in the selection process of admission to the M.Tech. (Research) programme in our department. Here are few more instructions to make it convenient for all of us.

1. The following Certificates shall be brought in ORIGINAL for verification purposes.
   (a) GATE Score Card
   (b) B.E / B.Tech Marks Cards and Degree Certificate
   (c) X and XII marks cards
   (d) Caste Certificate (if applicable)
   (e) Valid proof of photo identity.
   (Note: For OBC, the certificate must be dated on or after 1st April 2025. Please make sure that the caste name appear in the central list published by National Commission of Backward Classes (NCBC) Govt. of India at the link: www.ncbc.nic.in. **If the caste is not listed by NCBC or the certificate date is prior to 1st April 2025, then the OBC candidate will be considered under Open Category Only.** The OBC certificate format is available through the institute website (www.nitk.ac.in). For EWS Category, the Certificate should be as per prescribed format issued by the Competent Authority on or after 01-04-2025).

2. Please visit the Department Website (www.eee.nitk.ac.in) for updates and instructions which will be released by the department from time to time. The Department will not entertain any such concerns by the candidates regarding unawareness of the updates and instructions published in the department website.

3. Non-Production of Original Documents for Verification Purposes will disqualify the candidate from selection process.

4. All eligible candidates will be allowed to compete in the written test, which is of multiple-choice type questions. **The topics for the written test are the syllabus of GATE (EE-Electrical Engineering) exam.**

5. The Candidates are required to produce reprints of their publications, original certificates of work-experience and training-undergone at the time of Interview.

6. The Candidates are required to be available on 9th **December 2025** at Dept. of E & E, NITK, Surathkal for completion of the selection process and be available on mobile call for receiving and providing necessary information.

7. The candidates have to comply with the instructions published in the information bulletin for Ph.D & M.Tech Programme 2025-26 (December Session), regarding admission procedure as stipulated by the Institute.

8. For any clarifications contact Dr. A. Karthikeyan, Secretary, DRPC, through e-mail: jakarthik@nitk.edu.in or Phone : +91-824-2473554 (Office), +91-8867236461 (Cell).

(HoD & Chairman DPGC, EED)
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