Dept. of Electrical and Electronics Engineering, NITK Surathkal.
Ph.D. Admission July-2019
List of Candidates called for Written Test

Sl. No.	Application ID	Name of The Candidate
1	P19EE0001	SAURABH PRAKASH SAKALKALE
2	P19EE0002	PARVATHY M L
3	P19EE0006	VIDHYA SAGAR D
4	P19EE0007	UBAIDULLA
5	P19EE0008	PRATAP KUMAR KOPPOLU
6	P19EE0009	PRABHAT KUMAR SONU
7	P19EE0010	VEERALA GEETHA
8	P19EE0011	PRASHANT KUMAR
9	P19EE0015	SHEETAL CYRIAC
10	P19EE0016	KAMSALI LOKESH ACHARI
11	P19EE0017	MAHESH OBANNVAR
12	P19EE0018	RAMA SURYA RAO NANDIPATI
13	P19EE0020	A MERISELVAM
14	P19EE0021	MEKAPATI SURESH REDDY
15	P19EE0023	AJEYA K
16	P19EE0028	ANUBHAV KUMAR PANDEY
17	P19EE0030	VEMIREDDY NAGAMALLESWARI
18	P19EE0032	KANUMURI DEEPAK
19	P19EE0033	PONUKUMATI BALAMURALI KRISHNA
20	P19EE0037	BHAGYASREE
21	P19EE0042	ADITYA KUMAR
22	P19EE0044	MERLIN MARY N J
23	P19EE0048	SANTANU KUMAR PARIDA
24	P19EE0049	MARIA KUMAR THELAGTHOTI
25	P19EE0051	KARAN PARMAR
26	P19EE0052	ANKITA SEBASTIAN
27	P19EE0054	APARNAMOL N A
28	P19EE0055	MATANGI SAIDULU
29	P19EE0058	CHAITRASHREE C N
30	P19EE0060	PRAKASH C
31	P19EE0065	AKSHATA A KOTHARKAR
32	P19EE0066	S KUMARAN

33	P19EE0067	MANJUNATHA M N
34	P19EE0068	GUGULOTHU SRINIVASRAO
35	P19EE0070	MAHIPAL BUKYA
36	P19EE0073	RONANKI VENU NAIDU
37	P19EE0074	BALAJI S
38	P19EE0076	BRIJESH P V
39	P19EE0077	DONGARA RAMESH
40	P19EE0078	SUPRABHA PADIYAR
41	P19EE0082	KESARI HANUMANTHU
42	P19EE0085	BAIRABATHINA SAIKUMAR
43	P19EE0086	EDUKULLA RAMU
44	P19EE0088	HARSH WARDHAN PANDEY
45	P19EE0089	SAKHARE ABHILASH RAMESH

A specimen copy of the call letter along with detailed instructions regarding the selection procedure is given in the following pages. Please go through them carefully before appearing for the written test.

31/05/2019   Dr. Shubhanga K. N.

                                                      HOD, Dept. of E & E

DEPARTMENT OF ELECTRICAL AND ELECTRONCS ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL,
SRINIVASNAGAR, MANGALORE – 575 025

Tel : 0824-2473045, 2474054
Fax : 0824-2474039
Website : www.eee.nitk.ac.in

No.: NITK/EED/ Ph.D. /2019-20/8012   Date : 31-May-2019

OFFICIAL MEMORANDUM

Sub: Ph.D. Programme - Selection / Admission for the Year 2019-20; Intimation for Written Test and Interview.

To : Mr/ Ms. _____ Application No:_____

With reference to your application for admission to Ph.D. Programme in the Department of Electrical and Electronics Engg; you are requested to appear for a written test before the selection committee (at your own cost). After the written test, shortlisted candidates will be called for an interview. You should produce all records such as Date of Birth Certificate, Degree Certificate, Marks Cards of all years/semesters (U.G. and P.G. Programmes), Class 10th & 12th, GATE Score Card, Diploma Marks Card, Sponsorship certificate (if applicable), SC/ST Certificate (if applicable), OBC Certificate issued on or after 1st April 2019 (if applicable), Persons with Disabilities (PWD) certificate (if applicable), Conduct certificate and Testimonials, and valid photo identity card, **(All these originals)**. Please bring a self-attested photocopy of each of these certificates to submit at the time of interview.

Department	: Electrical and Electronics
Place of Written test & Interview	: Dept. of Electrical and Electronics Engg., Western Side Campus, NITK, Surathkal
Registration	: 17-06-2019, 08:00 AM
Written Test	: 17-06-2019, 08:30 AM to 11:30 AM
Document Verification	: 17-06-2019, 03:00 PM at Seminar Hall, Dept. of E & E
Announcement of Shortlisted Candidates for Interview*	: 17-06-2019, 05.30 PM
*(provisional; pending document verification)	
Interview	: 18-06-2019, 8.30 AM onwards at Conference Room, Dept. of E & E

Note :
1. **Please see the institute eligibility criteria given in the advertisement, carefully. It is the sole responsibility of the candidate to ascertain his/her eligibility before coming for the interview. The final confirmation of your eligibility is done only after the verification of original certificates.**
2. Candidates should come prepared to appear for a written Research-Aptitude Test conducted before the interview. Candidates are required to go through the list of Instructions to Candidates which is enclosed herewith.
3. Candidates must bring the "**Data Collection Format**" (enclosed) **duly filled** and submit at the time of registration/verification of documents (8.00 am) on 17th June 2019.
4. Sponsored candidates (Full time/Part time) should have been serving in the sponsoring organization for a period of at least 2 years after qualifying degree and have to produce a letter from their employer stating that the candidate is deputed for Research Programme (Full time / Part time) of the Institute on **full salary,** during the study period. The employer should indicate that the candidate will not be withdrawn midway before the completion of the course. (Sponsorship letter should be in the format provided in the Application Form).
5. **CANDIDATES WHO HAVE NOT SUBMITTED MARKS OF FINAL EXAMINATION ALONG WITH APPLICATION FORM SHALL PRODUCE THE SAME AT THE TIME OF ADMISSION. HOWEVER, CANDIDATES WHO HAVE WRITTEN FINAL YEAR EXAMINATIONS AND ARE YET TO OBTAIN FINAL SEMESTER MARK CARDS SHOULD SUBMIT THE SAME ON OR BEFORE 15-09-2019.**
6. Candidates who have qualified from the other Universities (Other than NITK) have to produce Migration certificate in order to validate their admission at a later stage.
7. **The selected candidates are required to pay fees and get admitted during the period 08-07-2019 to 12-07-2019. Fee details are as given in the "Information Bulletin 2019-20 (M.Tech and PhD)".**

(Dr. Shubhanga K. N., HoD & Chairman DRPC, EED)

NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA-SURATHKAL
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Date : 31st May 2019

INSTURCTIONS TO CANDIDATES APPLIED FOR Ph.D. PROGRAMME.

Dear Candidates,

You are invited to participate in the selection process of admission to the Ph.D. programme in our department. Here are few more instructions to make it convenient for all of us.

1. The following Certificates shall be brought in ORIGINAL for verification purposes.
   (a) GATE Score Card
   (b) B.E / B.Tech and M. Tech/M.E. Marks Cards and Degree Certificates
   (c) X and XII marks cards
   (d) Caste Certificate (if applicable)
   (e) Valid proof of photo identity.
   (Note : For OBC, the certificate must be dated on or after 1st April 2019. Please make sure that the caste name appears in the central list published by National Commission of Backward Classes (NCBC) Govt. of India at the link: www.ncbc.nic.in. **If the caste is not listed by NCBC or the certificate date is prior to 1st April 2019, then the OBC candidate will be considered under Open Category Only.** The OBC certificate format is available through the institute website (www.nitk.ac.in)

2. The syllabus for Written Test and Interview will be based on subjects covered in basic electrical engineering discipline and few general aptitude questions. The details of the courses and contents of the UG/PG programmes of Dept. of Electrical and Electronics, NITK, Surathkal are available through the department website: www.eee.nitk.ac.in.

3. Please visit the Department Website (www.eee.nitk.ac.in) for updates and instructions which will be released by the department from time to time. The Department will not entertain any such concerns by the candidates regarding unawareness of the updates and instructions published in the department website.

4. Non-Production of Original Documents for Verification Purposes will disqualify the candidate from selection process.

5. The procedure for selection is given on Page 3 of this document. The Candidates are required to go through the procedure carefully before appearing for the selection process.

6. The candidates are informed to download and fill up the Data Collection Format (Page 4 to 7) carefully and submit during Document Verification. This data will be used for preparation of the merit list.

7. The Candidates are required to produce reprints of their publications, original certificates of work-experience and training-undergone at the time of Interview.

8. The Candidates are required to be available for two days (17th and 18th June 2019) at Dept. of E & E, NITK, Surathkal for completion of the selection process and be available on mobile call for receiving and providing necessary information.

9. The candidates have to comply with the instructions published in the information bulletin for Ph.D & M.Tech. Programme 2019-20, regarding admission procedure as stipulated by the Institute.

10. For any clarifications contact Dr. Nagendrappa H., Secretary, DRPC, through e-mail: nagendrappa@gmail.com or Phone : +91-824-2473452 (Office), +91-9483830071 (Cell).

(Dr. Shubhanga K. N., HoD & Chairman DRPC, EED)

Selection Procedure:

Factors considered for selection: The selection of the scholars is based on all the following components considered at different stages as indicated in the *Procedure of Selection*:

1. Application in the prescribed format with all necessary data and submission of further information/ data sought by the department at the time of registration and interview.
2. Performance in the U.G and P.G. degrees.
3. Performance in the written test.
4. Performance in the interview.
5. *Experience and publications to be assessed during the interview.*

*Procedure of Selection (at the department level): The selection will involve **three stages**.*

Stage-1: All eligible candidates will be allowed to compete in the written test. A short-list of candidates for interview will be formed by considering the performance at the UG, PG, and the written test. Cut-off will be decided considering the number of seats permitted by the institute or the capacity of the department whichever is lower and applying a discount for different categories (OBC, SC, ST, and PWD). This is detailed in the *Procedure of short-listing for Interview*.

Note: The written test will be in the morning session on 17th June 2019 starting at 08:30 AM. Late entries are not allowed. Those who miss this scheduled-test will not be further considered for the selection process.

Stage-2: The short-listed Candidates in Stage-1, will be Interviewed by the Doctoral Research Programme Committee (DRPC). The DRPC will prepare a list of candidates recommending for admission to Ph.D. programme based on the performance in the interview, experience, and publications. This list will be submitted to the Head of the Department. This may be a NULL list in case no candidate is found suitable.

Stage-3: The Head of the Department will allocate the candidates to the Faculty members by consulting the candidates and the Faculty members intending to take the Ph. D. scholars, and forward the recommendation to the Dean (A).

Procedure of short-listing of candidates for Interview:

Step-1: All the Candidates meeting the institute eligibility criteria will be called for participating in the selection process.

Step-2: A written test will be conducted on the first day of the process to get a departmental level assessment of the candidates.

Step-3: A merit order will be formed for short-listing of candidates for interview giving weightages for different components as follows:

Undergraduate performance (First to final semester)	Postgraduate performance (First and second semester)	Written test	Total
30 %	20 %	50 %	100 %

Step-4: If the department has **N** number of seats in "Open Category (OC)", then the cut-off Rank (**R**) for OC will be "3 times **N**" in the merit order formed in Step-3 OR 25% <u>whichever is higher</u>. This list (L1) may be represented by the candidates from all categories (OC, OBC, SC, ST and PWD) and has a Cut-off mark M1 corresponding to the Rank R.

Step-5: The cut-off mark (M2) for OBC candidates is fixed at 90% of the Cut-off mark (M1) for OC. This list is L2 and contains all OBC candidates having score between and including M1 and M2 but limited by 3 times the number of seats available in OBC category.

Step-6: The cut-off mark (M3) for SC candidates is fixed at 75% of the Cut-off mark (M1) for OC. This list is L3 and contains all SC candidates having score between and including M1 and M3 but limited by 3 times the number of seats available in SC category.

Step-7: The cut-off mark (M4) for ST candidates is fixed at 70% of the Cut-off mark (M1) for OC. This list is L4 and contains all ST candidates having score between and including M1 and M4 but limited by 3 times the number seats available in ST category.

Step-8: The Shortlist of candidates for the interview is L= L1+ L2+L3+L4.

(Dr. Shubhanga K. N., HoD & Chairman DRPC, EED)

Data Collection format
Admission to Ph.D. Programme for the Academic Session July-Dec. 2019

Name of the Candidate			Application No.	

Email id: | | | **Mobile No.** |

PG		(Awarded Marks in %) or Grades/Scale		Equivalent % age marks	
	PG (up to 2 semesters)	CGPA (C1G)		(C1G/Scale)*100	
		C1=%age		C1	
	PG (Aggregate of all semesters)	CGPA (C2G)		(C2G/Scale)*100	
		C2=%age		C2	

GATE (* Declared out of 100 in GATE score card)	Year	Marks(*)	Marks Normalized(*)

UG (Degree awarded)	(Awarded Marks in %) or Grades/Scale (aggregate of 8 semesters)		Equivalent %age Marks (B=)	
	CGPA (C4G)		(C4G/Scale)*100	
	C4=%age		C4	

10th Level (CGPA/ Marks)		12th level (CGPA/ Marks)	
Diploma (CGPA/ Marks)			

Remarks (CGPA of 6.0/60%) (CGPA 5.5/55%: SC/ST/PWD)(@ PG levels) CGPA Scale:	QUALIFIED		NOT QUALIFIED	

Experience and Publications to be considered during the interview.

	Teaching	Industry	Others (Specify)	Remarks
Experience (in years)				
	Journals	Conferences	Books	Remarks
Publications (Numbers)				

Declaration by the Candidate: I hereby state that I have understood all the instructions provided so far and that I have taken all the necessary care in providing the information in the Data Collection Form. I will be solely responsible for all the consequences arising out of submission of incorrect data, the one that is being used in the selection process.

Signature of the candidate
Date: 17-06-2019
Place: NITK, Dept. of EE

Verified by: Signature
Name:
Designation: Faculty Member/ Staff

PG- Degree

Name of the candidate							Application No.						
	Course Details				Result			Course Details				Result	
Semester	Course code	Credits	Max. Marks/	Grade	Grade Point	Marks	Semester	Course code	Credits	Max. Marks/	Grade	Grade Point	Marks
1							2						
3							4						
CGPA (in case of Grades) upto 2nd Semester		C1G					CGPA (in case of Grades) upto 4th Semester		C2G				
Percentage (in case of Marks) upto 2nd Semester		C1					Percentage (in case of Marks) upto 4th Semester		C2				

Signature of the Candidate:
Date:
Place: NITK, Dept. of EE

Verified by: Signature
Name:
Designation: Faculty Member/ Staff

UG (Semesters 1 to 4) (First Attempt Grades/ Marks only to be used)

| Name of the candidate | | | | Application No | | | |

Sem.	Course Details			Results			Sem.	Course Details			Results		
	Course Code	Credits	Max. Marks	Grade	Grade Point	Marks		Course Code	Credits	Max. Marks	Grade	Grade Point	Marks
1							2						
3							4						

Signature of the candidate
Date: 17-06-2019
Place: NITK, Dept. of EE

Verified by: Signature
Name:
Designation: Faculty Member/ Staff

UG (Semesters 5 to 8) (First Attempt Grades/ Marks only to be used)

Name of the candidate					Application No					

Sem.	Course Details			Results			Sem.	Course Details			Results		
	Course Code	Credits	Max. Marks	Grade	Grade Point	Marks		Course Code	Credits	Max. Marks	Grade	Grade Point	Marks
5							6						
7							8						

Signature of the candidate   Verified by: Signature
Date: 17-06-2019   Name:
Place: NITK, Dept. of EE   Designation: Faculty Member/ Staff