NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL
DEPARTMENT OF ELECTRICAL & ELECTRONICS, NITK, Surathkal

Selection of Candidates for Admission to PhD Programme, 2022-23 (December Session)

### List of Candidates Called for Written Test

Date of Written Test: 12th December 2022, 2.30 to 3.30 PM

Venue: Dept. of E&E Conference Room

Sl. No.	Name	Reference Number
1	SWAPNIL RAMESH REVANKAR	PH2022EE0045
2	GUTTI OM SURAJ	PH2022EE0046
3	MOHAMED NADIR N	PH2022EE0047
4	SHIV SHANKAR SINHA	PH2022EE0048
5	ROHINI	PH2022EE0049
6	BIKKAVALLI SURYA TEJA	PH2022EE0050
7	NEHA KUMARI	PH2022EE0051
8	NEETHU S	PH2022EE0053
9	SUPRITHA M R	PH2022EE0054
10	SOUMYA K T	PH2022EE0055

29|11|22

(Dr. Tukaram Moger)
Secretary, DRPC (EE)
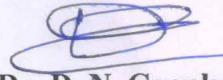
(Dr. D. N. Gaonkar)
Chairman, DRPC (EE)

ASSOC. PROFESSOR & HEAD
DEPT. OF ELECTRICAL & ELECTRONICS ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA
SURATHKAL SRINIVASNAGAR P.O., MANGALORE-575025

DEPARTMENT OF ELECTRICAL AND ELECTRONCS ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL,
SRINIVASNAGAR, MANGALORE – 575 025

Tel : 0824-2473045, 2474054
Fax : 0824-2474039
Website : www.eee.nitk.ac.in

No. PhD/2022-23/E6

Date: 29th December 2022

To the shortlisted candidates:

With reference to your application for admission to **Ph.D. Programme for the year 2022-23 (December Session) in the Department of Electrical and Electronics Engg.,** you are requested to appear for a **written test** before the selection committee (at your own cost). After the written test, shortlisted candidates will be called for an **interview**. You should produce all records such as Date of Birth Certificate, Degree Certificate, Marks Cards of all years/semesters (U.G. and P.G. Programmes), Class 10th & 12th, GATE Score Card, Diploma Marks Card, Sponsorship certificate (if applicable), SC/ST Certificate (if applicable), OBC Certificate issued on or after 01 April 2022 (if applicable), EWS Certificate issued on or after 01 April 2022 (if applicable), Persons with Disabilities (PWD) certificate (if applicable), Conduct certificate and Testimonials, and valid photo identity card, **(All originals)**. Please bring a self-attested photocopy of each of these certificates to submit at the time of interview.

Department	: Electrical and Electronics
Place of Written test & Interview	: Dept. of Electrical and Electronics Engg., Western Side Campus, NITK, Surathkal
Registration	: 12-12-2022, 09:30 AM at Seminar Hall, Dept. of E & E
Document Verification	: 12-12-2022, 10.00 AM Onwards at Seminar Hall, Dept. of E & E
Written Test	: 12-12-2022, 2.30 to 3.30 PM at Seminar Hall, Dept. of E & E
Announcement of Shortlisted Candidates for Interview* *(provisional; pending document verification)	: 12-12-2022, 05.30 PM
Interview	: 13-12-2022, 9.30 AM onwards at Conference Room, Dept. of E & E

Note:

1. **Please see the institute eligibility criteria given in the advertisement, carefully. It is the sole responsibility of the candidate to ascertain his/her eligibility before coming for the interview. The final confirmation of your eligibility is done only after the verification of original certificates.**
2. Candidates should come prepared to appear for a written Research-Aptitude Test conducted before the interview. Candidates are required to go through the list of Instructions to Candidates which is enclosed herewith.
3. Candidates must bring the "Data Collection Format" (enclosed) **duly filled** and submit at the time of registration/verification of documents on 12th December 2022.
4. Full-time/External Registrants - sponsored from Industry or other organizations including Educational Institutions, **should have been serving in the sponsoring organization for a period of at least 2 years after qualifying degree** and have to produce a letter from their employer stating that the candidate is deputed for Research Programme (Full time/Part time) of the Institute on **full salary,** during the study period. The employer should indicate that the candidate will not be withdrawn midway before the completion of the course. (Sponsorship letter should be in the format provided in the Application Form).
5. Candidates who have got the degree(s) from the other Universities (Other than NITK) have to produce Migration certificate in order to validate their admission at a later stage.
6. The Selected candidates are required to pay fees and physically report for admissions on any of the following dates: **22nd Dec. 2022, 23rd Dec. 2022 and 26th Dec. 2022.**
7. For more information about the programme and fees, please refer "Information_Bulletin-PhD-2022 (December session)".

(HoD & Chairman DRPC, EED)
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NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA-SURATHKAL
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

<u>Date: 29th December 2022</u>

<u>INSTURCTIONS TO CANDIDATES APPLIED FOR Ph.D. PROGRAMME.</u>

Dear Candidates,

You are invited to participate in the selection process of admission to the Ph.D. programme in our department. Here are few more instructions to make it convenient for all of us.

1. The following Certificates shall be brought in ORIGINAL for verification purposes.
    (a) GATE Score Card
    (b) B.E / B.Tech and M. Tech/M.E. Marks Cards and Degree Certificates
    (c) X and XII marks cards
    (d) Caste Certificate (if applicable)
    (e) Valid proof of photo identity.
    (Note: For OBC, the certificate must be dated on or after 1st April 2022. Please make sure that the caste name appear in the central list published by National Commission of Backward Classes (NCBC) Govt. of India at the link: www.ncbc.nic.in. **If the caste is not listed by NCBC or the certificate date is prior to 1st April 2022, then the OBC candidate will be considered under Open Category Only.** The OBC certificate format is available through the institute website (www.nitk.ac.in). For EWS Category, the Certificate should be as per prescribed format issued by the Competent Authority on or after 1-4-2022).

2. The syllabus for Written Test and Interview will be based on subjects covered in basic electrical engineering discipline and few general aptitude questions. The details of the courses and contents of the UG/PG programmes of Dept. of Electrical and Electronics, NITK, Surathkal are available through the department website: www.eee.nitk.ac.in.

3. Please visit the Department Website (www.eee.nitk.ac.in) for updates and instructions which will be released by the department from time to time. The Department will not entertain any such concerns by the candidates regarding unawareness of the updates and instructions published in the department website.

4. Non-Production of Original Documents for Verification Purposes will disqualify the candidate from selection process.

5. The procedure for selection is given on *Page 3* of this document. The Candidates are required to go through the procedure carefully before appearing for the selection process.

6. The candidates are informed to download and fill up the Data Collection Format (*Pages 4 to 7*) carefully and submit during Document Verification. This data will be used for preparation of the merit list.

7. The Candidates are required to produce reprints of their publications, original certificates of work-experience and training-undergone at the time of Interview.

8. The Candidates are required to be available for two days (**12th and 13th December 2022**) at Dept. of E & E, NITK, Surathkal for completion of the selection process and be available on mobile call for receiving and providing necessary information.

9. The candidates have to comply with the instructions published in the information bulletin for Ph.D & M.Tech Programme 2022-23 (December Session), regarding admission procedure as stipulated by the Institute.

10. For any clarifications contact Dr. Tukaram Moger, Secretary, DRPC, through e-mail: tukaram@nitk.edu.in or Phone : +91-824-2473451 (Office), +91-9611475268 (Cell).

(HoD & Chairman DRPC, EED)

Admission to PhD Programme for the year 2022-23 (December Session): Selection Procedure

Factors considered for selection: The selection of the scholars is based on all the following components considered at different stages as indicated in the *Procedure of Selection*:
1. Application in the prescribed format with all necessary data and submission of further information/ data sought by the department at the time of registration and interview.
2. Performance in the U.G and P.G. degrees.
3. Performance in the written test.
4. Performance in the interview.
5. Experience and publications to be assessed during the interview.

Procedure of Selection (at the department level): The selection will involve three stages.

Stage-1: All eligible candidates will be allowed to compete in the written test, which is of multiple-choice type questions, with only one correct answer and with negative marking. A short-list of candidates for interview will be formed by considering the performance at the UG, PG, and the written test. Cut-off will be decided considering the number of seats permitted by the institute or the capacity of the department whichever is lower, and applying a relaxation for different categories (OBC, SC, ST, and PWD). This is detailed in the *Procedure of short-listing for Interview.*

Stage-2: The short-listed Candidates in Stage-1, will be Interviewed by the Doctoral Research Programme Committee (DRPC). The DRPC will prepare a list of candidates recommending for admission to Ph.D. programme based on the performance in the interview, experience, and publications. This list will be submitted to the Head of the Department. This may be a NULL list in case no candidate is found suitable.

Stage-3: The Head of the Department will allocate the candidates to the Faculty members by looking into the candidate's research interest and the research area of Faculty members intending to take the Ph. D. scholars, and forward the recommendation to the Dean (A).

Procedure of short-listing of candidates for Interview:

Step-1: All the Candidates meeting the institute eligibility criteria will be called for participating in the selection process.

Step-2: A written test will be conducted on the first day of the process to get a departmental level assessment of the candidates.

Step-3: A merit order will be formed for short-listing of candidates for interview giving weightages for different components as follows:

Undergraduate performance (First to final semester)	Postgraduate performance (First to final semester)	Written test	Total
30 %	20 %	50 %	100 %

Step-4: If the department has N number of seats in the Open Category (OC), then the cut-off mark ($M1$) for OC will be the mark of $(3N)^{th}$ candidate in the merit order formed in Step-3. This list ($L1$) may be represented by the candidates from all categories (OC, EWS, OBC, SC, ST and PWD).

Step-5: The cut-off mark ($M2$) for OBC candidates is fixed at 90% of the Cut-off mark ($M1$) for OC. This list is $L2$ and contains all OBC candidates having score between $M1$ and $M2$ (included), but limited by 3 times the number of seats available in OBC category.

Step-6: The cut-off mark ($M3$) for SC candidates is fixed at 75% of the Cut-off mark ($M1$) for OC. This list is $L3$ and contains all SC candidates having score between $M1$ and $M3$ (included), but limited by 3 times the number of seats available in SC category.

Step-7: The cut-off mark ($M4$) for ST candidates is fixed at 70% of the Cut-off mark ($M1$) for OC. This list is $L4$ and contains all ST candidates having score between $M1$ and $M4$ (included), but limited by 3 times the number of seats available in ST category.

Step-8: The shortlist of candidates for the interview is $L=L1 + L2 + L3 + L4$ + other student types (full-time/part-time sponsored etc.).
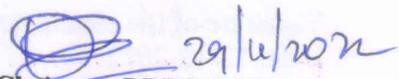
29/4/2022

(HoD & Chairman DRPC, EED)

Data Collection format
Admission to Ph.D. Programme for the year 2022-23 (December Session)

Name of the Candidate				Application No.		
Email id:				Mobile No.		

PG			Awarded Marks / Grades	Equivalent % age marks		
	PG (up to 2 semesters)	CGPA (C1G)		(C1G-0.5)*10		
		C1=%age		C1		
	PG (Aggregate of all semesters)	CGPA (C2G)		(C2G-0.5)*10		
		C2=%age		C2		

GATE (* Declared out of 100 in GATE score card)	Year	Marks(*)	Marks Normalized(*)

UG (Degree awarded)	Awarded Marks / Grades (aggregate of 8 semesters)		Equivalent %age Marks (B=)	
	CGPA (C4G)		(C4G – 0.5)*10	
	C4=%age		C4	

10th Level (CGPA/ Marks)		12th level (CGPA/ Marks)	
Diploma (CGPA/ Marks)			
Remarks (CGPA of 6.0/60%) (CGPA 5.5/55%: SC/ST/PWD)(@ PG levels)	QUALIFIED	NOT QUALIFIED	

Experience and Publications to be considered during the interview.				
Experience (in years)	Teaching	Industry	Others (Specify)	Remarks
Publications (Numbers)	Journals	Conferences	Books	Remarks

Declaration by the Candidate:

I hereby state that I have understood all the instructions provided so far and that I have taken all the necessary care in providing the information in the Data Collection Form. I will be solely responsible for all the consequences arising out of submission of incorrect data, the one that is being used in the selection process.

Signature of the candidate
Date: 12-12-2022
Place: NITK, Dept. of EE

Verified by: Signature
Name:
Designation: Faculty Member/ Staff

PG- Degree

Name of the candidate							Application No.						
	Course Details			**Result**				**Course Details**			**Result**		
Semester	Course code	Credits	Max. Marks/	Grade	Grade Point	Marks	Semester	Course code	Credits	Max. Marks/	Grade	Grade Point	Marks
1							2						
3							4						
CGPA (in case of Grades) upto 2nd Semester	C1G						CGPA (in case of Grades) upto 4th Semester	C2G					
Percentage (in case of Marks) upto 2nd Semester	C1						Percentage (in case of Marks) upto 4th Semester	C2					

Signature of the Candidate: Verified by: Signature
Date: 12-12-2022   Name:
Place: NITK, Dept. of EE   Designation: Faculty Member/ Staff

UG (Semesters 1 to 4) (First Attempt Grades/ Marks only to be used)

Name of the candidate						Application No					
Sem.	Course Details			Results			Sem.	Course Details			Results		
	Course Code	Credits	Max. Marks	Grade	Grade Point	Marks		Course Code	Credits	Max. Marks	Grade	Grade Point	Marks
1							2						
3							4						

Signature of the candidate   Verified by: Signature
Date: 12-12-2022   Name:
Place: NITK, Dept. of EE   Designation: Faculty Member/ Staff

UG (Semesters 5 to 8) (First Attempt Grades/ Marks only to be used)

Name of the candidate						Application No					
Sem.	**Course Details**			**Results**		**Sem.**	**Course Details**			**Results**	
	Course Code	Credits	Max. Marks	Grade	Grade Point	Marks		Course Code	Credits	Max. Marks	Grade	Grade Point	Marks
5							**6**						
7							**8**						

Signature of the candidate
Date: 12-12-2022
Place: NITK, Dept. of EE

Verified by: Signature
Name:
Designation: Faculty Member/ Staff